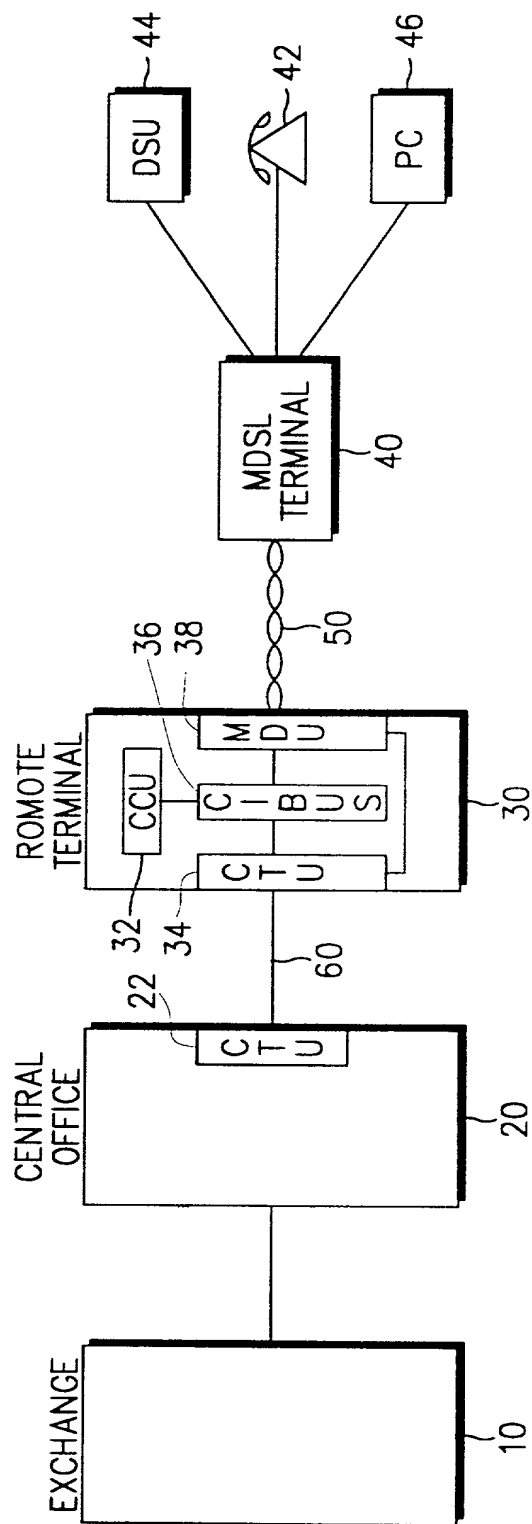


FIG. 1



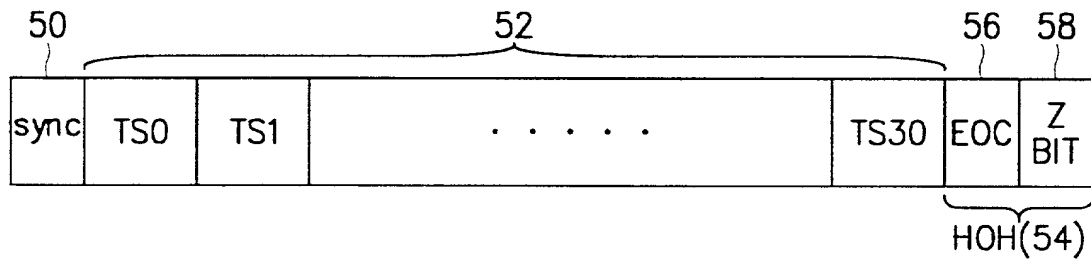


FIG. 2

octet	<u>IDLC MODE</u>				
4	ACT1	1	MB1	RING TYPE	RING1
5	ACT2	1	MB2	RING TYPE	RING2
6	CHECK-SUM				

FIG. 3A

octet	<u>UDLC MODE</u>						
4	ACT1	0	MB1	A1	B1	C1	D1
5	ACT2	0	MB2	A2	B2	C2	D2
6	CHECK-SUM						

FIG. 3B

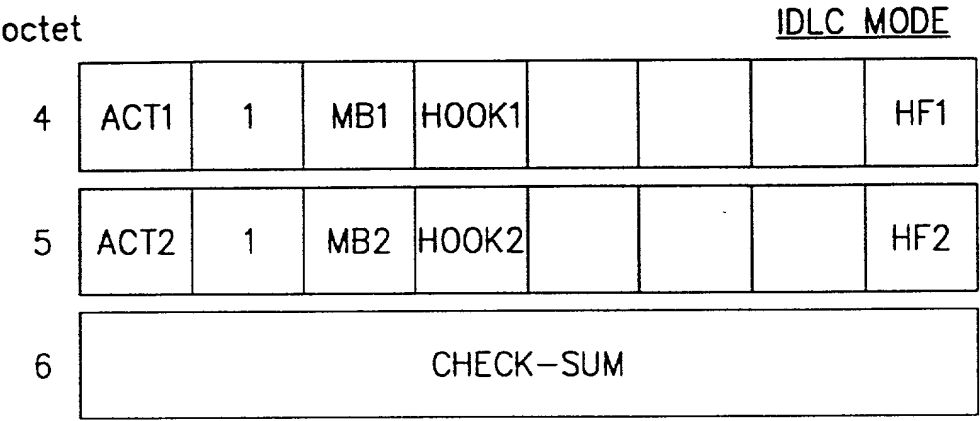


FIG. 4A

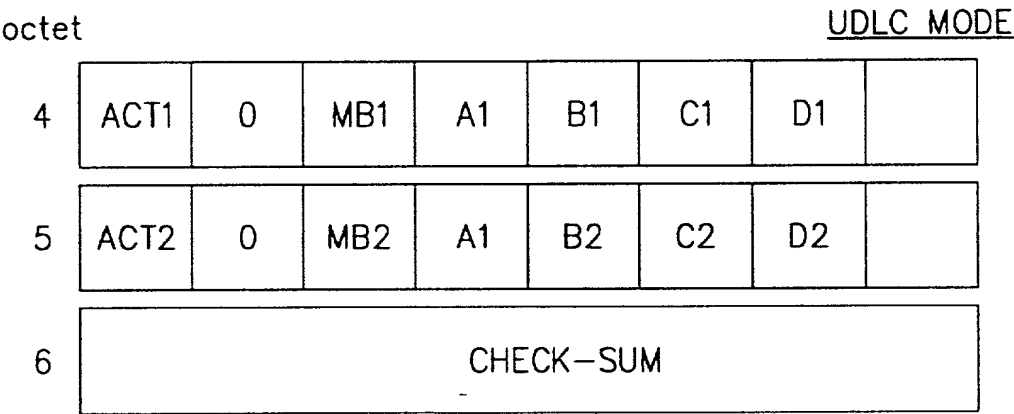


FIG. 4B

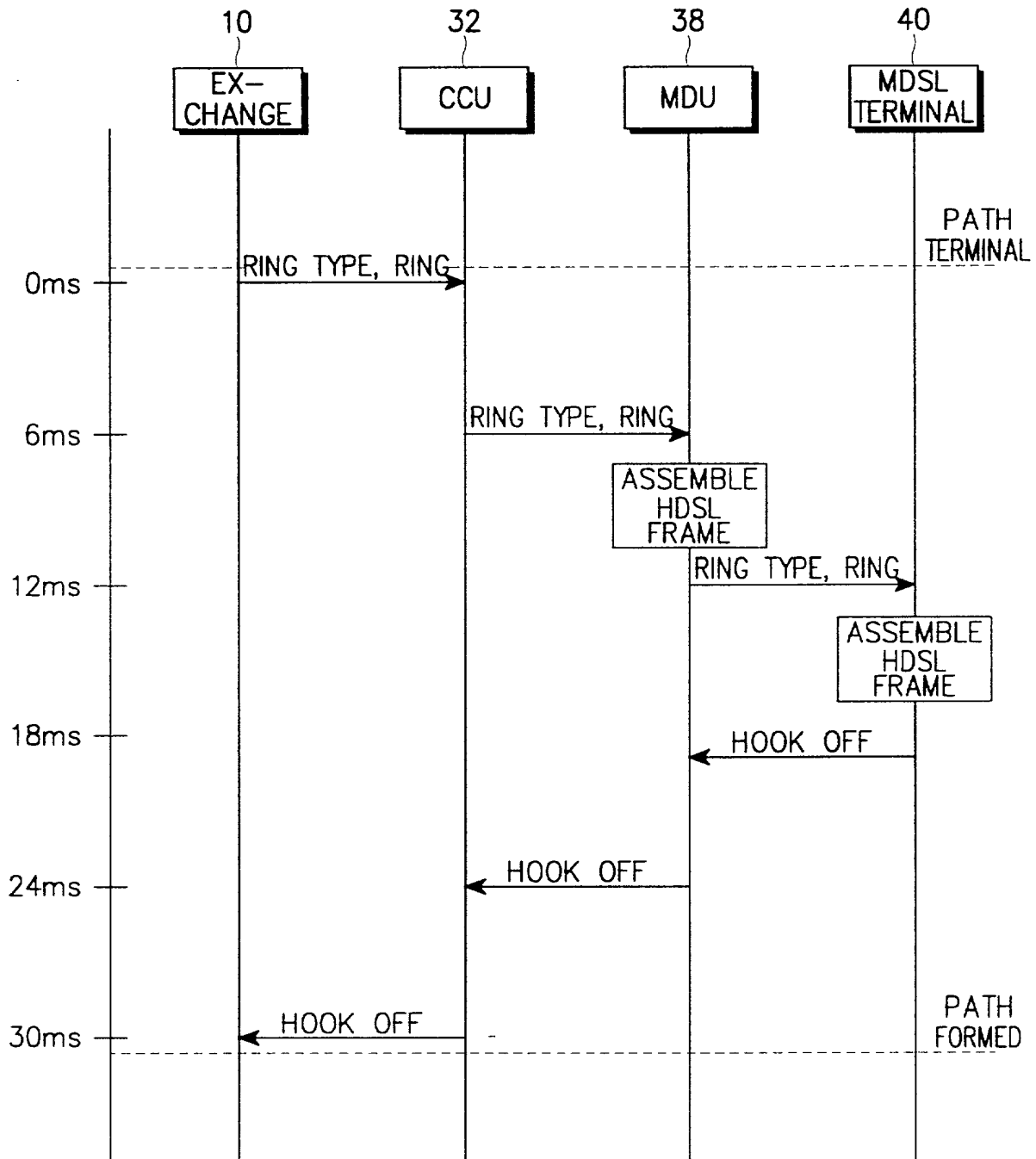


FIG. 5